

Substitute for form 1449A/PTO

Complete if Known

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1

of

Application Number

10/758,547

Filing Date

January 14, 2004

First Named Inventor

Anna Chamy

Art Unit

Unassigned

Examiner Name

Unassigned

Attorney Docket Number

CISCP844

U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/DS/	AA	US-5,864,540	01-01-1999	Bonomi et al.	
/DS/	AB	US-6,130,878	10-10-2000	Chamy	
/DS/	AC	US-6,408,005	06-18-2002	Fan et al.	
	AD	US-			
	AE	US-			
	AF	US-			
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	AH	US-			
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	AJ	US-			
	AK	US-			
	AL	US-			
	AM	US-			
	AN	US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	AO					<input type="checkbox"/>
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	AS					<input type="checkbox"/>
	AT					<input type="checkbox"/>
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Signature

/Dewanda Samuel/

Date
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06/18/2007

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 1

Complete if Known

Application Number	10/758,547
Filing Date	January 14, 2004
First Named Inventor	Anna Charny
Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	CISCP844

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/DS/	A	JON C.R. BENNETT et al., "Hierarchical Packet Fair Queuing Algorithms", IEEE ACM Transactions on Networking, 5(5), pp. 675-689, Oct. 1997	
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/DS/	E	P. GOYAL et al., "Start-time fair queuing: a scheduling algorithm for integrated services packet switching networks", Proc. Sigcomm, 1996	
/DS/	F	CHRIS KAPPLER et al., "HQF Implementation Based on Sorting Trees (HIST)", Cisco Systems, Doc. No. EDCS-273780 - rev 3.9, pp. 1-123	
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/DS/	L	ION STOICA et al., "Earliest Eligible Virtual Deadline First: A Flexible and Accurate Mechanism for Proportional Share Resource Allocation", Department of Computer Science, Old Dominion University, Norfolk, Virginia, TR-95-22, January 26, 1998, pp. 1-37	
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/DS/	N	K. THOMPSON et al., "Wide-Area Internet Traffic Patterns and Characteristics", IEEE/ACM Transactions on Networking, pp. 10-23, November 1997	
/DS/	O	T. TUAN et al., "Multiple Time Scale Congestion Control for Self-Similar Network Traffic", Performance Evaluation, 36:359-386, 1999	

Examiner Signature	/Dewanda Samuel/	Date Considered	06/18/2007
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Sheet 1

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Complete if Known

Application Number	10/758,547
Filing Date	January 14, 2004
First Named Inventor	Anna Charny
Art Unit	2665
Examiner Name	Huy Duy Vu
Attorney Docket Number	CISCP844

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
/DS/ ↓	A	US- 2005/0152374	07-14-2005	Cohen et al.	
	B	US- 2006/0029079	02-09-2006	Cohen et al.	
	C	US- 2006/0029080	02-09-2006	Kappler et al.	
	D	US- 2006/0153243	07-13-2006	Shoham et al.	
	E	US- 6,560,230	05-06-2003	Li et al.	
	F	US- 6,691,312	02-10-2004	Sen et al.	
	G	US- 6,836,475	12-28-2004	Chaskar et al.	
	H	US- 6,412,000	06-25-2002	Riddle et al.	
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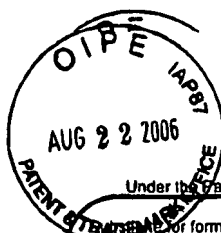
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NON PATENT LITERATURE DOCUMENTS

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/DS/	I	Bennett et al., "Hierarchical Packet Fair Queuing Algorithms," IEEE/ACM Transactions on Networking (TON), Vol. 5, Issue 5, Oct. 1997, pp. 675-689.	
/DS/	J	Hou et al., "Service Disciplines for Guaranteed Performance Service," Fourth Int'l Workshop on Real-Time Computing Systems and Applications, Oct. 27-29, 1997, pp. 244-250, Taipei.	
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/DS/	N	Bennett et al., "WF2Q: Worst-case Fair Weighted Fair Queuing," INFOCOM '96 Fifteenth Annual Joint Conference of the IEEE, Mar. 24-28, 1996, Vol. 1, pp.120-128.	
/DS/	O	Bennett et al., "High-Speed, Scalable, and Accurate Implementation of Fair Queuing Algorithms in ATM Networks," ICNP '97, http://www-2.cs.cmu.edu/~hzhang/papers/ICNP97.pdf , 1997.	

Examiner Signature	/Dewanda Samuel/	Date Considered	06/18/2007
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